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- ☐ 2. **Interstitial redundancy: an area efficient fault tolerance scheme for large area VLSI process**
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- ☐ 3. **On yield consideration for the design of redundant programmable logic arrays**
 Wey, C.-L.;
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[Abstract](#) | Full Text: [PDE\(620 KB\)](#) IEEE JNL
- ☐ 4. **Comment on 'Nonplanar VLSI arrays with high fault-tolerance capabilities'**
 Koren, I.; Pradhan, D.K.;
 Reliability, IEEE Transactions on
 Volume 38, Issue 5, Dec. 1989 Page(s):527
 Digital Object Identifier 10.1109/24.46475
[Abstract](#) | Full Text: [PDE\(152 KB\)](#) IEEE JNL
- ☐ 5. **Reconfiguration of VLSI arrays by covering**
 Lombardi, F.; Sami, M.G.; Stefanelli, R.;
 Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on
 Volume 8, Issue 9, Sept. 1989 Page(s):952 - 965
 Digital Object Identifier 10.1109/43.35547
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- ☐ 6. **Nonplanar VLSI arrays with high fault-tolerance capabilities**
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- ☐ **23. Performance evaluation of a reconfiguration-algorithm for memory arrays containing clusters**
Blough, D.M.;
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Volume 45, Issue 2, June 1996 Page(s):274 - 284
Digital Object Identifier 10.1109/24.510815
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- ☐ **24. Minimum fault coverage in memory arrays: a fast algorithm and probabilistic analysis**
Chor-Ping Low; Hon Wai Leong;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on
Volume 15, Issue 6, June 1996 Page(s):681 - 690
Digital Object Identifier 10.1109/43.503937
[Abstract](#) | [Full Text: PDF\(1054 KB\)](#) [IEEE JNL](#)
- ☐ **25. RAID5 performance with distributed sparing**
Thomasian, A.; Jai Menon;
Parallel and Distributed Systems, IEEE Transactions on
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**1. Redundant arrays of IDE drives**

Sanders, D.A.; Cremaldi, L.M.; Eschenburg, V.; Lawrence, C.N.; Riley, C.; Summers, D.J.; Petravic
Nuclear Science, IEEE Transactions on
Volume 49, Issue 4, Part 1, Aug. 2002 Page(s):1834 - 1840
Digital Object Identifier 10.1109/TNS.2002.801699

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Marcheschi, P.; Positano, V.; Ferdeghini, E.M.; Mazzarisi, A.; Benassi, A.;
Computers in Cardiology, 2003
21-24 Sept. 2003 Page(s):367 - 370
Digital Object Identifier 10.1109/CIC.2003.1291168

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Ferdeghini, E.M.; Marcheschi, P.; Mazzarisi, A.; Benassi, A.;
Computers in Cardiology, 2003
21-24 Sept. 2003 Page(s):363 - 366
Digital Object Identifier 10.1109/CIC.2003.1291167

[AbstractPlus](#) | Full Text: [PDF](#)(1460 KB) IEEE CNF**4. Redundant arrays of IDE drives**

Sanders, D.A.; Cremaldi, L.A.; Eschenburg, V.; Lawrence, C.N.; Riley, C.; Summers, D.J.; Petravic
Nuclear Science Symposium Conference Record, 2001 IEEE
Volume 1, 4-10 Nov. 2001 Page(s):515 - 518 vol.1

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Shiroumaru, I.; Nishi, T.; Yamada, I.; Watanabe, S.;
Power Engineering Society 1999 Winter Meeting, IEEE
Volume 1, 31 Jan.-4 Feb. 1999 Page(s):145 - 150 vol.1
Digital Object Identifier 10.1109/PESW.1999.747440

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